

#3/2-26-02
PATENT

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Serial No: 09/768,987 Docket No: 2011005
Filing Date: 01/23/01 Applicant: CHEN, W
Examiner: POTTER, R Art Unit: 2822
Title: STRUCTURE OF STACKED INTEGRATED CIRCUITS
AND METHOD FOR MANUFACTURING THE SAME

To: THE ASSISTANT COMMISSIONER
FOR PATENTS
Washington, D.C. 20231

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TECHNOLOGY CENTER 2800

RESPONSE TO OFFICE ACTION

Dear Sir:

In response to the Office Action dated 01/02/02, Applicant submits the following.

ELECTION

1. Applicant elects to restrict this application to the species designated by the Examiner as Species I, relating to Figs. 1-4, and directed to claims 1-6.

Respectfully submitted,

Date: January 25, 2002



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CERTIFICATE OF MAILING (37 CFR 1.8a): I hereby certify that this paper (along with any referred to as being attached or enclosed) is being deposited with the United States Postal Service on the date shown below with sufficient postage as First Class Mail in an envelope addressed to: The Assistant Commissioner for Patents, Washington, D.C. 20231.

Date: January 26, 2002



Keith Kline